



This is to Certify

OLYMPIA ELECTRONICS S.A.
Kolindros Pierias, Greece, 60061

is authorised to use the LPCB mark in association with the product(s) listed in this certificate and appendix.

BSR-2104 and BSR-2114

which have complied with the requirements of the following:

EN 54-2: 1997 + A1: 2006
EN 54-4: 1997 + A1: 2002 + A2: 2006

Certificate No: 1010a

Issue Number: 01

Date of Issue: 21 December 2010

Signed for and on behalf of LPCB

E MATTIE-SULEIMAN



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Appendix to Certificate

Certificate No: 1010a

OLYMPIA ELECTRONICS S.A.

Products

LPCB Ref. No.

BSR-2104 Two loop analogue addressable control and indicating equipment.

1010a/01

Incorporating the following units:

1702091	Main board
0905073	Main processor board
0506073	Input / Output Unit
1603073	LED PCB board
2307103	Loop 1 incorporating power control board
DPP100-24	Lambda PSU block

Certified with the following options with requirements from EN 54 part 2:

7.8	Output to fire alarm devices
7.9.1	Output to fire alarm routing equipment
7.10.1	Output to fire protection equipment (Type A)
7.10.4	Fault monitoring of fire protection equipment
7.11	Delays to outputs
7.13	Alarm counter
8.3	Fault signal from points
8.9	Output to fault warning routing equipment
9.5	Disablement of each address point
10	Test condition

Appendix Issue No: 01

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Date of Issue: 21 December 2010

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Appendix to Certificate

Certificate No: 1010a

OLYMPIA ELECTRONICS S.A.

Products

LPCB Ref. No.

BSR-2114 Four loop analogue addressable control and indicating equipment.

1010a/02

Incorporating the following units:

1702091	Main board
0905073	Main processor board
0506073	Input / Output Unit
1603073	LED PCB board
2307103	Loop 1 incorporating power control board
2607103	Loop 2 board
DPP100-24	Lambda PSU block

Certified with the following options with requirements from EN 54 part 2:

7.8	Output to fire alarm devices
7.9.1	Output to fire alarm routing equipment
7.10.1	Output to fire protection equipment (Type A)
7.10.4	Fault monitoring of fire protection equipment
7.11	Delays to outputs
7.13	Alarm counter
8.3	Fault signal from points
8.9	Output to fault warning routing equipment
9.5	Disablement of each address point
10	Test condition

Appendix Issue No: 01

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